∇ 321SAC1.

Example Schematic LUC02 CONTROLLER

DMX CONTROLLER INPUT (CAT5 RJ45) LED OUTPUT TO INPUT OF T PIECE Ο \cap \bigcirc UT IN LED −Dmmx= ♀ ™™ ♀ OUT DMX OUTPUT TO DMX INPUT OF NEXT LUD48/LUD192 0-48v **Note:** When using LUC02 with an LUD48/192, the controller output needs to www.abstractavr.com <u> Abstract</u> be set to 48v. LUĎ-48 8888 0000 ADDR Dmx Address OUTP Output Mode NODE Control Mode NACR Macro Mode LOAD Load Control OPCL Factory Reset CLR Clear Mode TST Test Mode DENO Demo Mode OFFS Othet Mode FADE Fade Mode TIME Time Mode PATT Falter Mode REC Record Mode PLAY Play Mode STAT Static Mode 00 LED 0 DO NOT REMOVE COVER NO USER SERVICEABLE PARTS INSIDE A MAINS INPUT 90-252V 50-60Hz 50-60Hz Ο ∥⊖ □ 240V MAINS

Mains 3 Core CAT5 RJ45 (DMX) CAT5 RJ45 (LED) WIFI N/A Highlight

